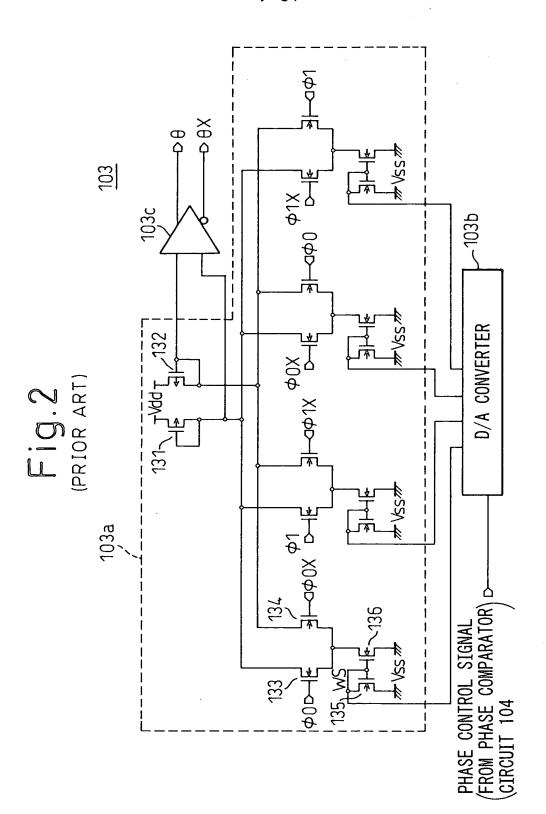


 $\frac{2}{31}$

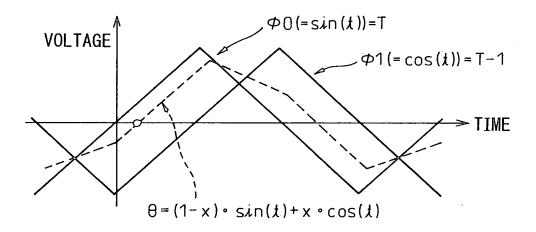


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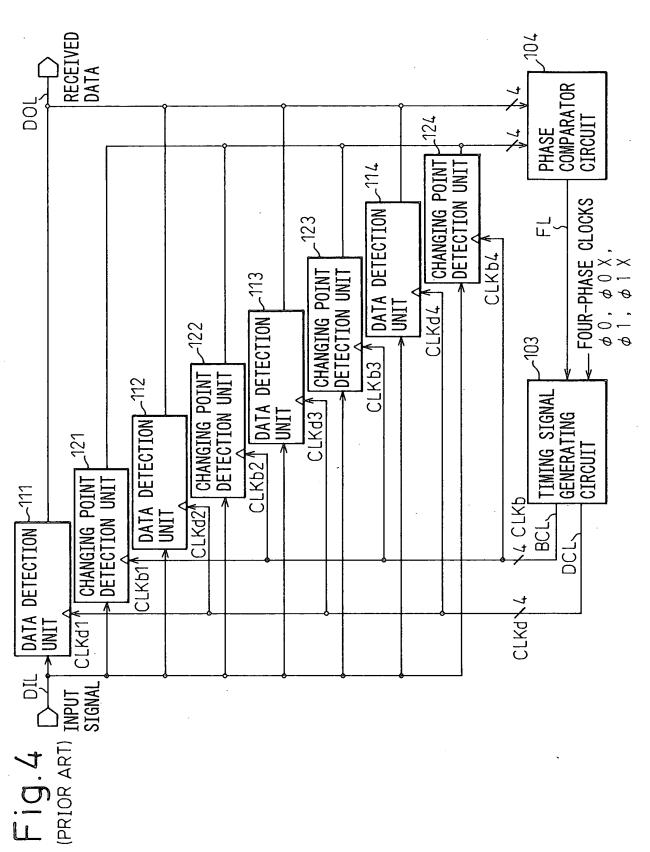
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 $\frac{3}{31}$

Fig.3
(PRIOR ART)

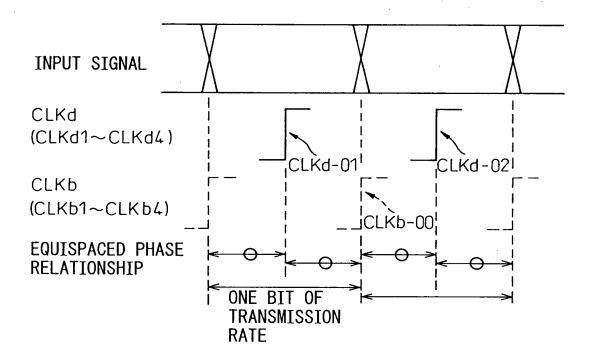


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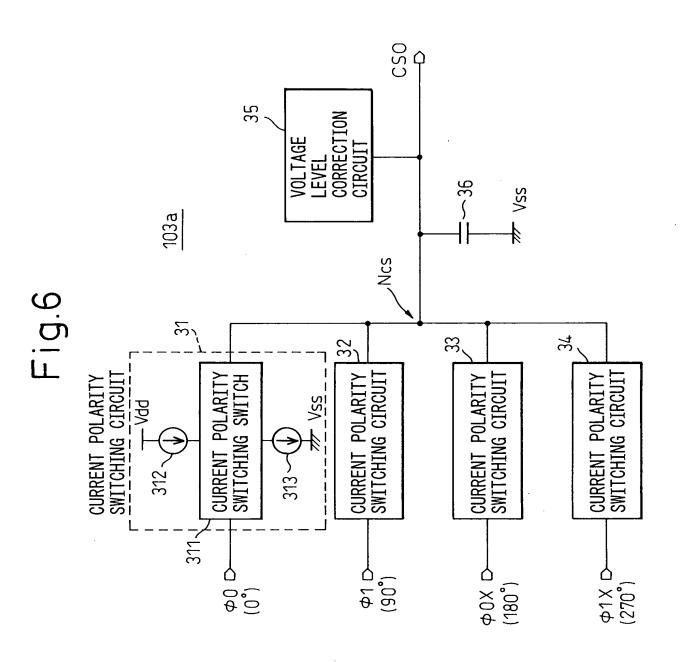
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Fig.5



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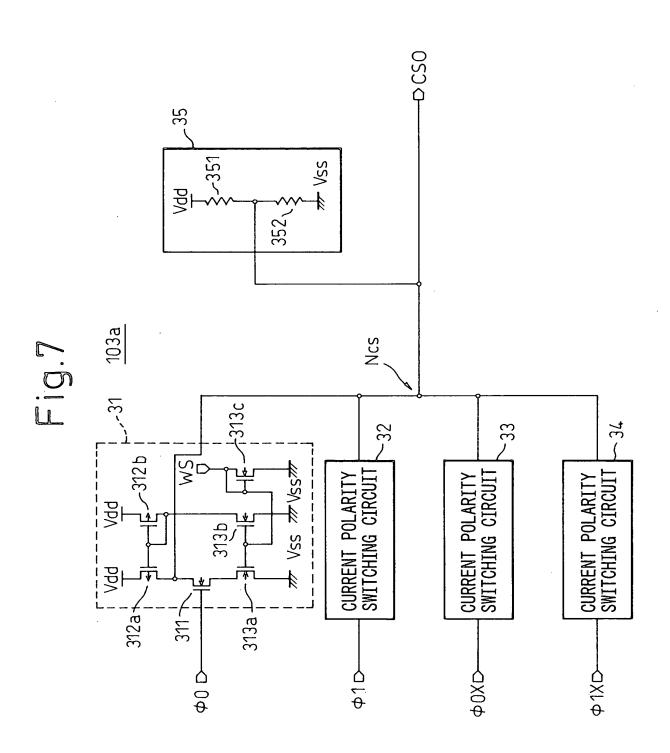




Fig.8A

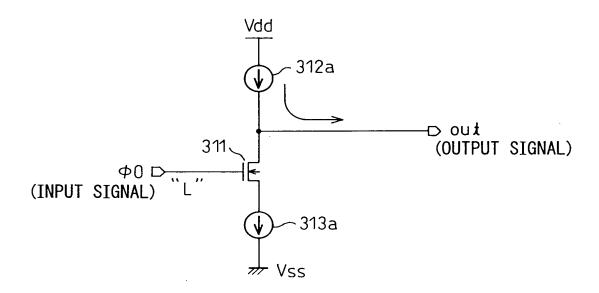
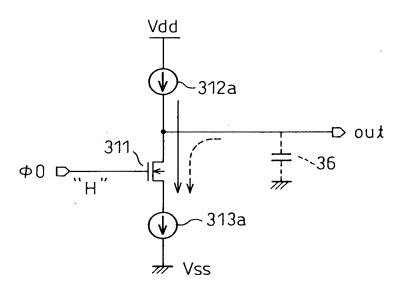


Fig.8B



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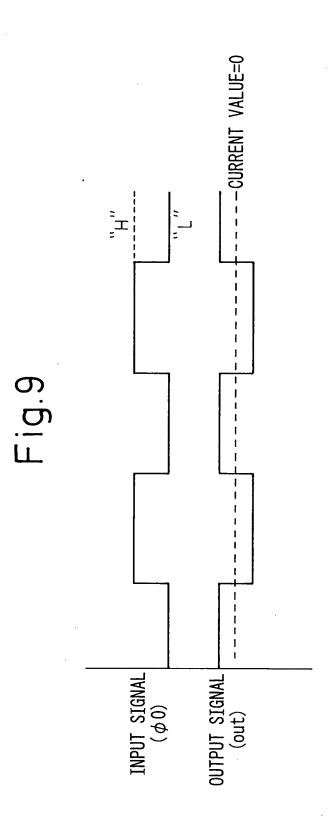


Fig.10

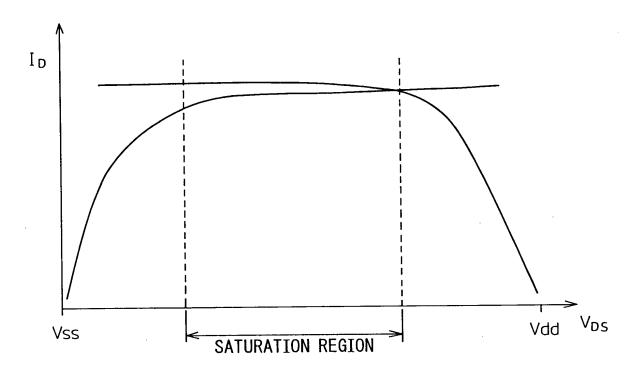
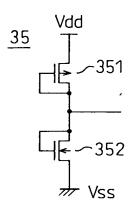
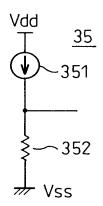


Fig.11A Fig.11B Fig.11C





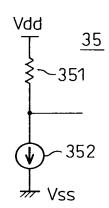


Fig.11D

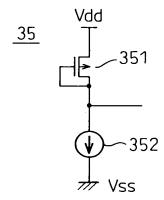
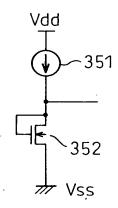


Fig.11E



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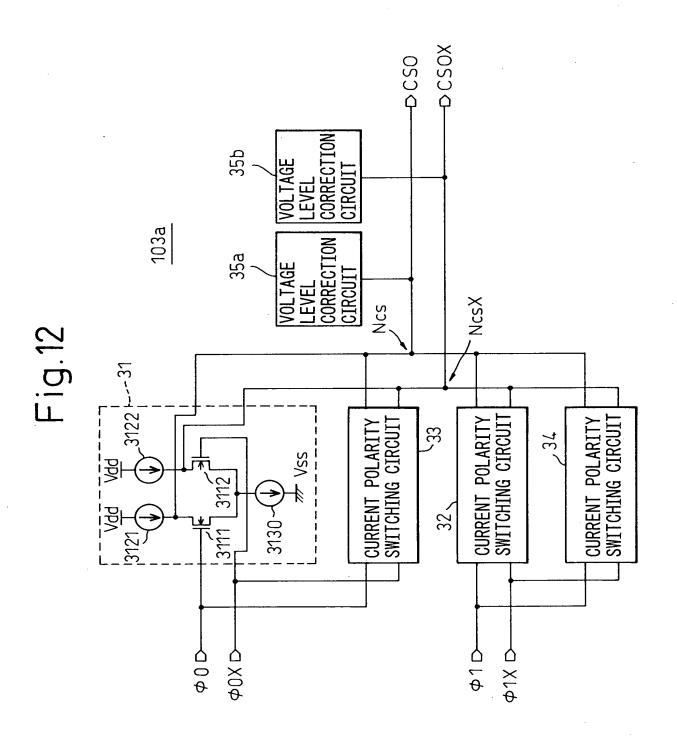
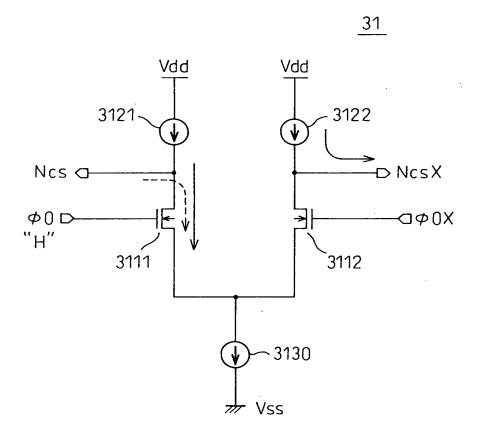
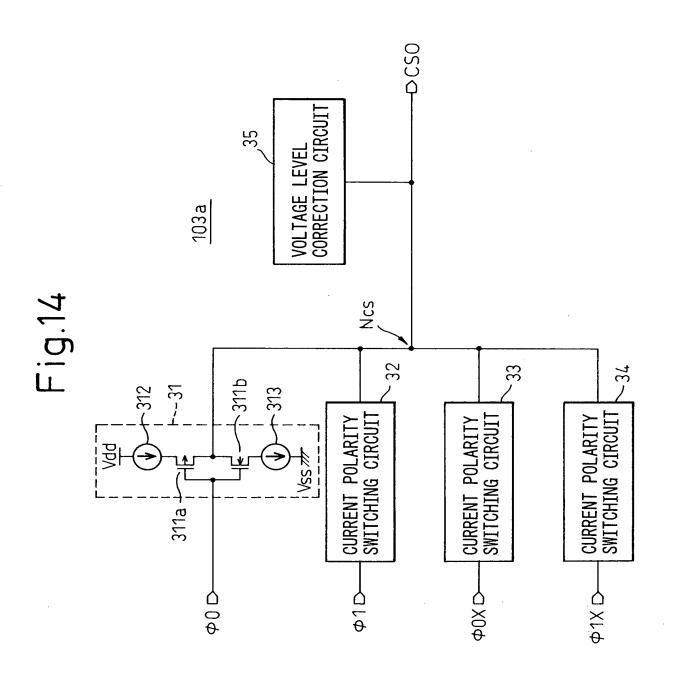


Fig.13



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Fig.15A

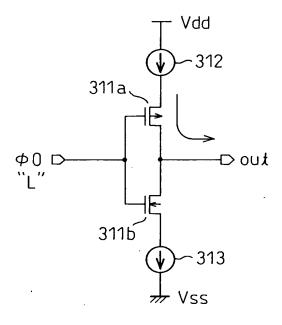
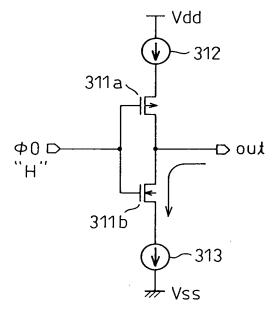
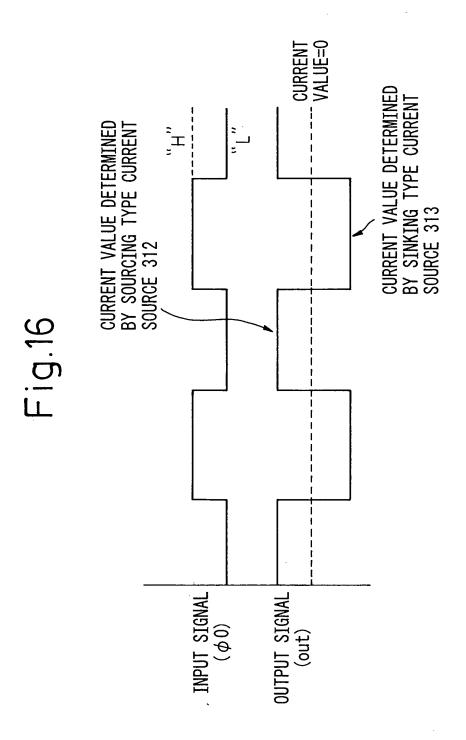


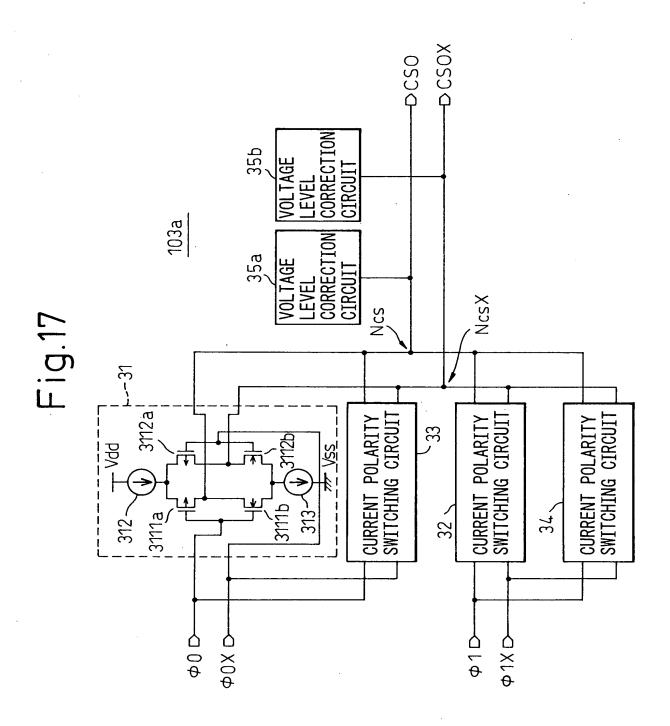
Fig.15B



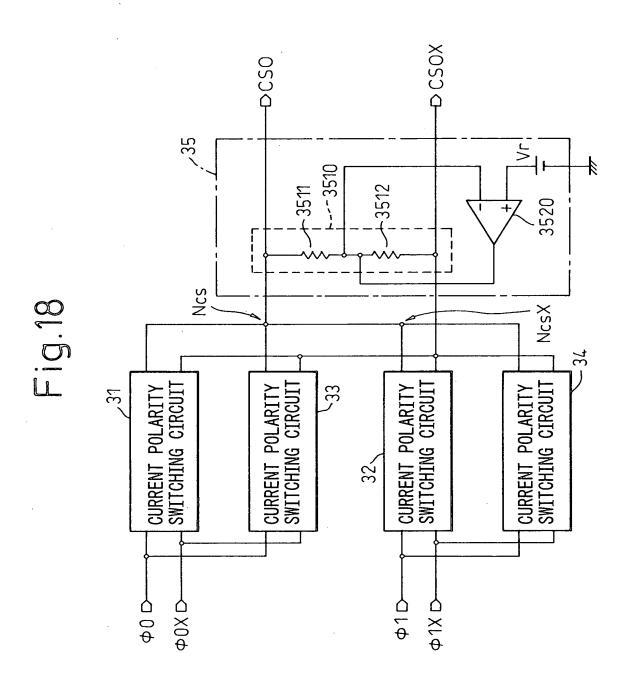
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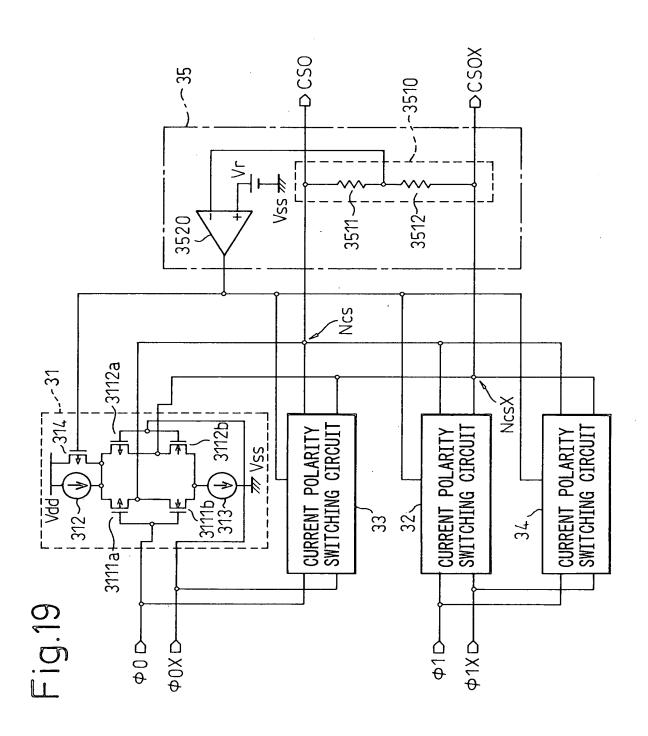
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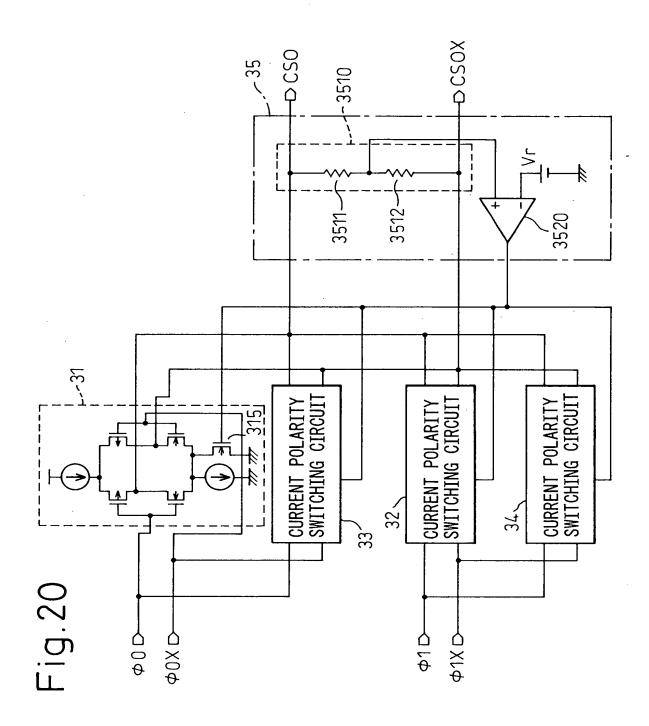
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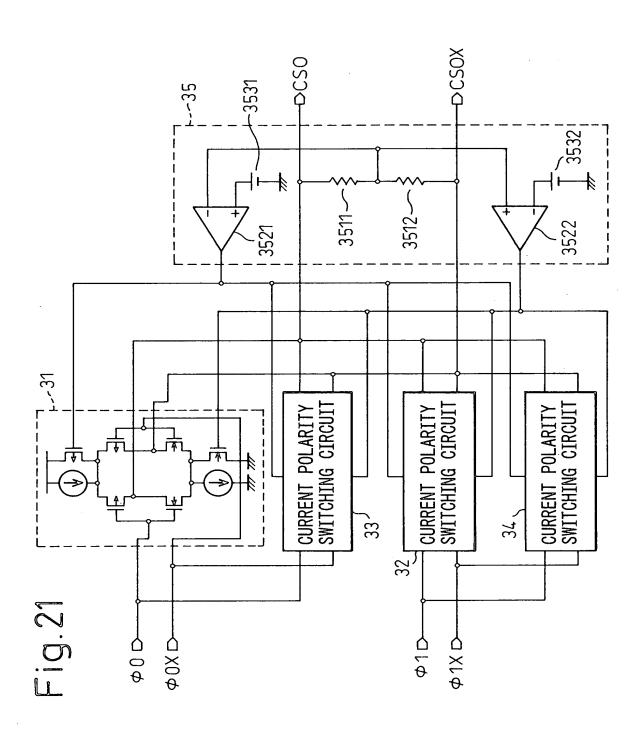
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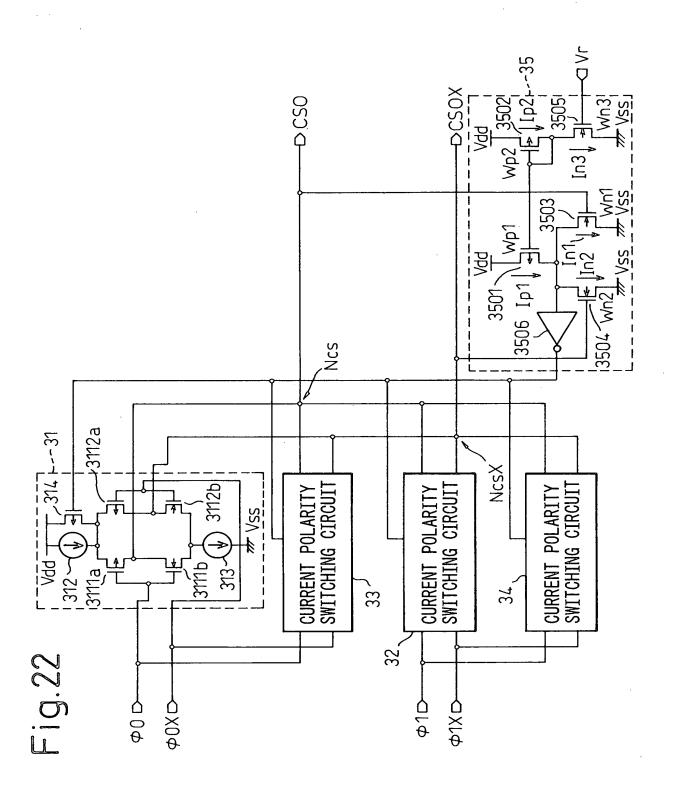
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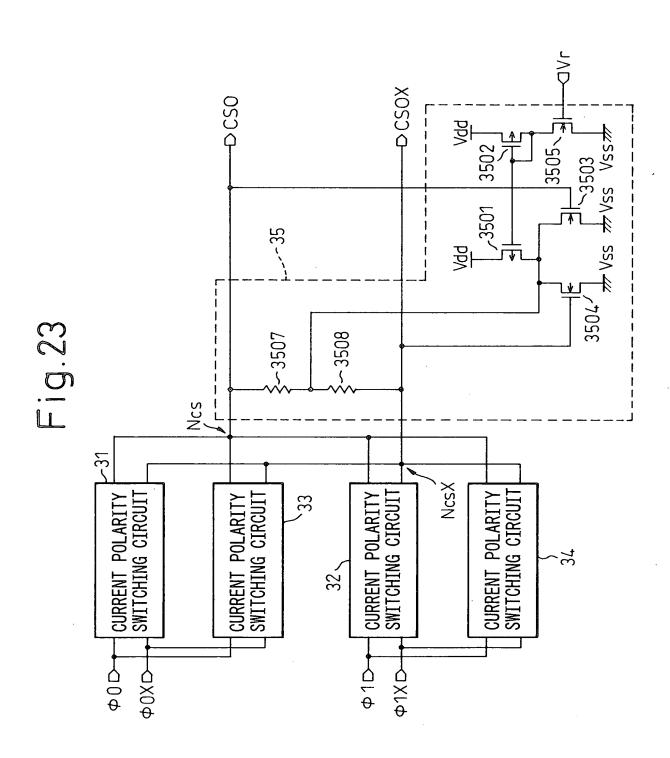
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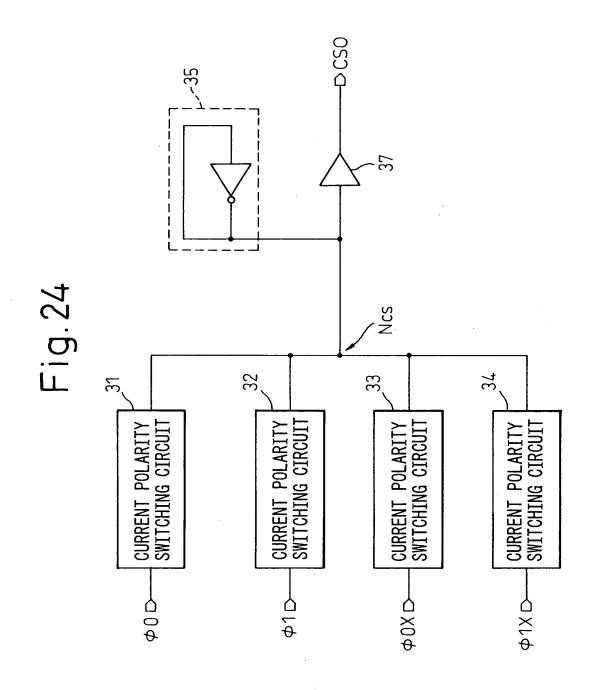


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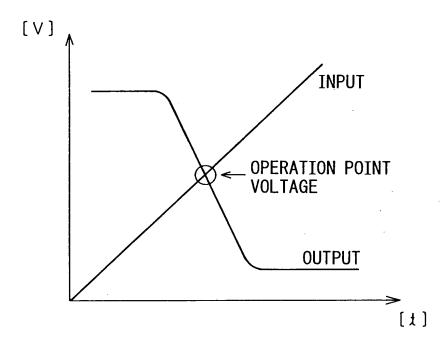




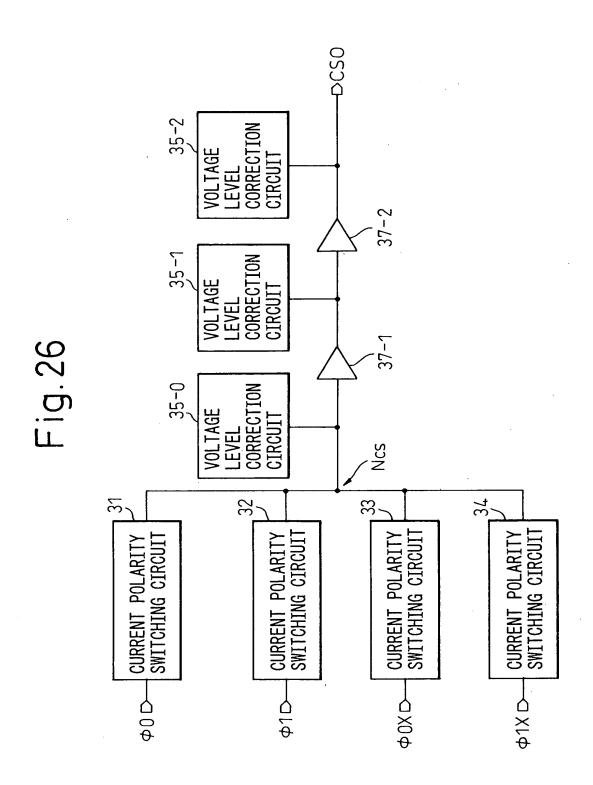
²⁵/₃₁

Fig.25

[DC CHARACTERISTICS OF AMPLIFIER]



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 $\frac{27}{31}$

Fig. 27A

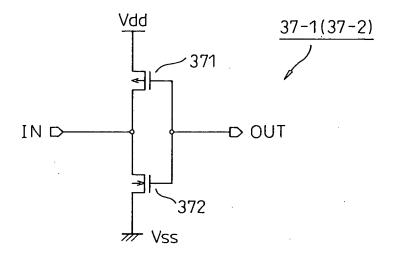
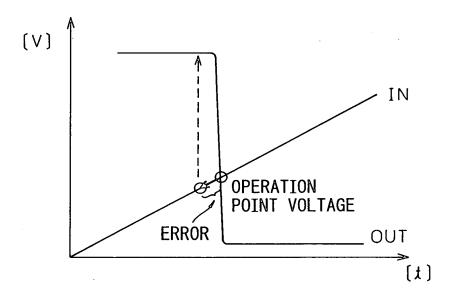


Fig.27B



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Fig.28A

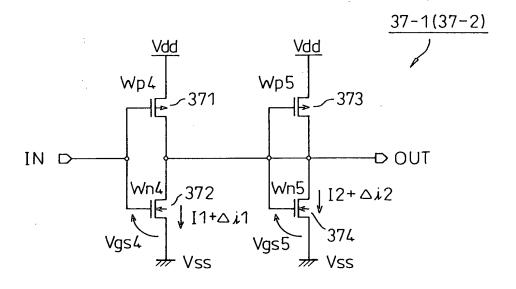
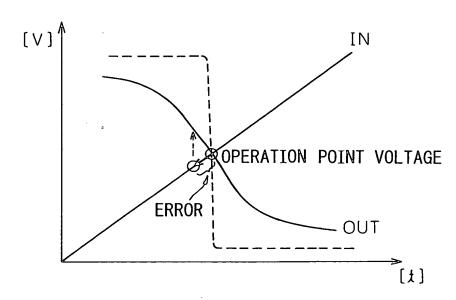
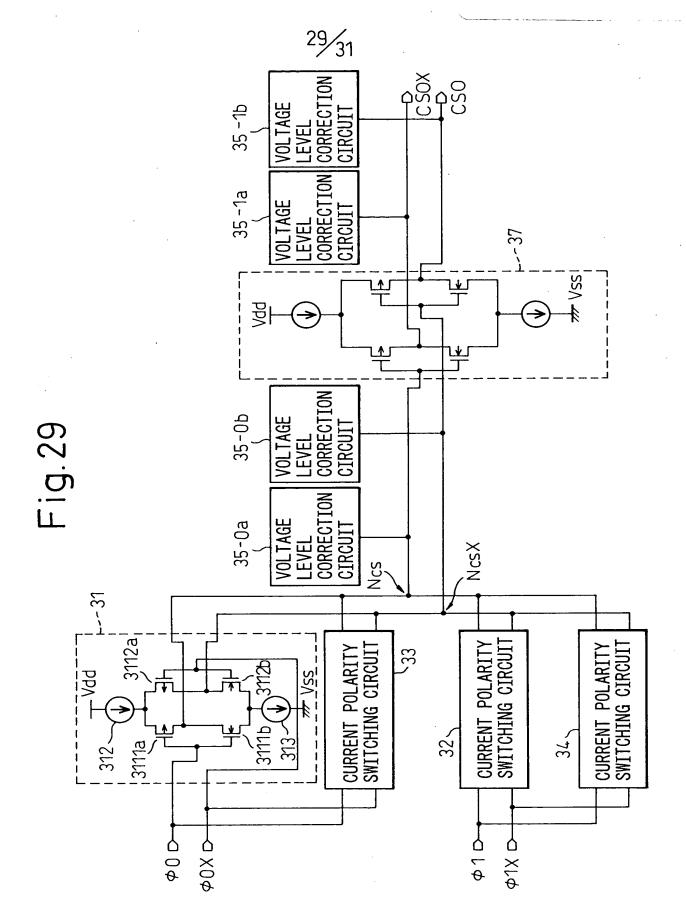
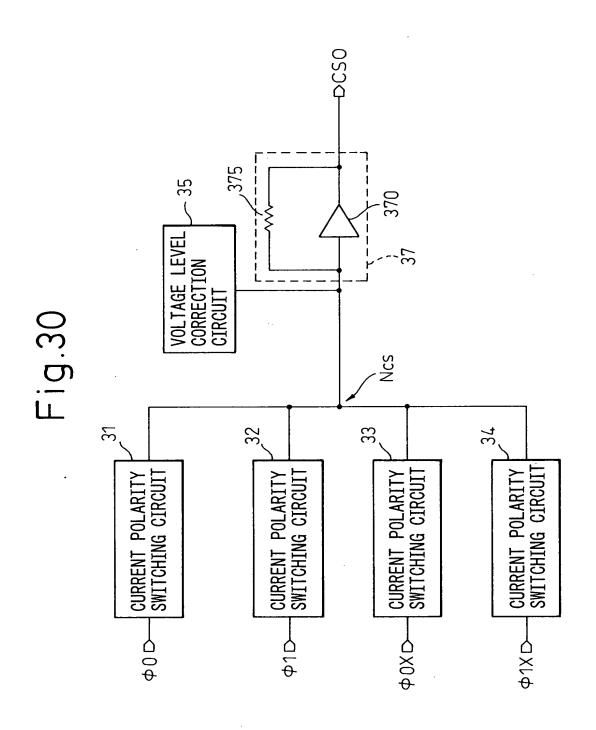


Fig.28B



Title: TIMING SIGNAL GENERATING CIRCUIT AND RECEIVER CIRCUIT DESIGNED TO SPEED UP SIGNAL TRANSMISSION Inventor: Takaya CHIBA et al. Docket Number: 100021-00137





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